

REMARKS

The drawings have been objected to under 37 CFR 1.83(a) as not showing every feature of the invention specified in the claims. More specifically, the Examiner alleges that the claimed “memory unit”, “test recording unit”, and “optimum tracking offset determination unit” are not shown in the submitted drawings. Applicant respectfully disagrees.

Claims 10 and 13 have been amended to make it clear that the memory unit is disclosed in the optical disk drive. The memory unit corresponds to ROM 24b as is shown in Figure 1 and not to the optical disk. This amendment is supported in the description of the specification on page 11, third paragraph which reads as follows:

“Further, a ROM 24b embedded in the microcomputer 24 stores therein tables such as a characteristic value table showing target characteristic values corresponding to a disk type (the ID number of a disk) and a rotative mode (single, double, 4 times, 6 times, 8 times, 12 times, and 16 times speed, etc.) a write power table of the OPC showing an initial write power and a step corresponding to a rotative mode, and a tracking offset value table showing an initial tracking offset value and a step value”.

The “test recording unit” as claimed corresponds to the microcomputer 24 shown in Figure 1, which operation is shown in steps S26 and S28 of the flow chart in Figure 4.

The “optimum tracking offset determination unit” as claimed is also part of the microcomputer 24 shown in Figure 1. It’s operation is shown in steps S30 and S32 of the flow chart in Figure 4. In this regard, claim 2 has also been amended to specify that the microcomputer includes a test recording unit and an optimum tracking offset determination unit thereby conforming the claim to Figures 1 and 4. Accordingly, the rejection to the drawings under 37 CFR 1.83(a) should

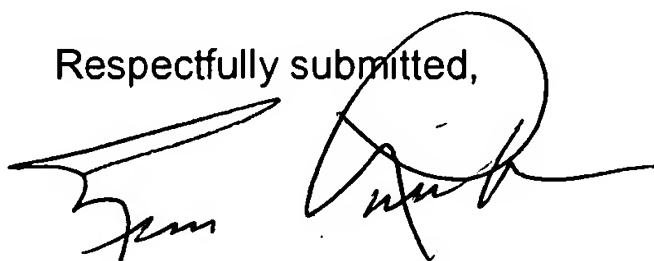
now be withdrawn.

The Examiner has indicated that claims 10-15 are allowable and that the application is otherwise in condition for allowance except for the objection to claims 10-15 relating to the terms "memory unit", "test recording unit" and "optimum tracking offset determination unit". As indicated above, these units are indeed supported by the description with the "test recording unit" and "optimum tracking offset determination unit" included in microcomputer 24 shown in Figure 1.

Accordingly, the application is now in condition for allowance.

Reconsideration and allowance of claims 10 - 15 is respectfully solicited.

Respectfully submitted,

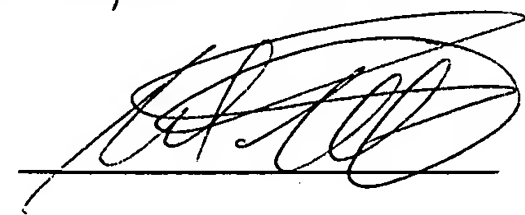


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MAILING CERTIFICATE

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed: Commissioner for Patents, P.O. Box 1450, Alexandria, VA, MAIL STOP AM, 22313-1450 on February 21, 2006.



Date: February 21, 2006